

REMARKS

Claims 1 and 3-10 are pending in this application. The applicant has cancelled claims 3 and 10 and amended claims 1 and 9 to overcome the Examiner's rejections. Similarly, the above amendments to the specification have been made to correct typographical errors and overcome the drawing and specification objections.

The Examiner acknowledged that claims 6-8 are allowable and dependent Claims 3-5 and 10 contain allowable subject matter. Independent Claims 1 and 9 have been amended to incorporate the allowable subject matter of claims 3 and 10, respectively. No new matter has been added to the claims by any of the amendments.

Attached hereto as pages 5 and 6 are marked-up versions of the changes made to claims 1 and 9 and the specification by the proposed amendments. The attachment is captioned "**Version with Markings to Show Changes Made.**"

A formal set of figures have been provided with this response.

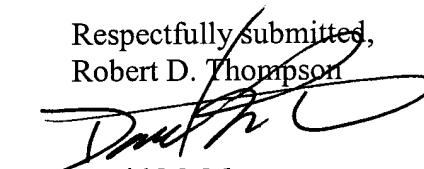
At this time, the applicant respectfully requests that the amendments are entered and a timely notice of allowance is issued in this case for all pending claims.

Please do not hesitate to contact the undersigned council for this application if the Examiner has any questions regarding this case.

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231, on _____

By _____ Date: _____
Terry Flores

Version with Markings to Show Changes Made

In the Claims:

Claims 3 and 10 has been cancelled.

The remaining claims have been amended as follows:

1. (Amended) 1. A method for calibrating a scanner being coupled to a host computer, the method comprising:
retrieving operational data from a memory element of the host computer;
evaluating the retrieved data for degradation in the operation of the scanner using a calculated percentage of change between past and present operational data; and
calibrating the scanner to overcome the degradations,
wherein the operational data includes information obtained from previous scans and calibrations.

9. (Amended) A system for calibrating a scanner being coupled to a host computer, the system comprising:
a retrieving means for obtaining operational data from a memory element of the host computer;
an evaluating means for analyzing the retrieved data for degradation in the operation of the scanner; and
a calibrating means for allowing the scanner to overcome the degradations,
wherein the operational data includes information obtained from previous scans and calibrations, and the evaluating means includes a calculating means to determine the percentage of change between past and present operational data.

In the specification

Please amend the specification as follows:

Page 9, lines 7-14, replace first full paragraph with:

--Slave controller 35 controls the timing of CCD sensor 21 and ADC 33 over control lines 46 and 48. Slave controller 35 receives the digitized data from ADC 33 over bus [52] 50, provides any required formatting and/or image processing and stores the digitized data in RAM 39 via bus 52. In addition, slave controller 35 manages the transfer of data from RAM 39 to a host (not shown) such as an image processing system or general purpose computer system via host interface 41. Master controller 37 is provided to initiate an image scanning or calibration operation and to set up and supervise slave controller 35 via bus 54.—

Page 9, line 20 thru Page 10, line 4, replace paragraph with:

-- DMA controller 67 controls the storage and removal of data from RAM 39. DMA controller 67 communicates with RAM 39 via bus 56 and with host interface 41 via bus 58. Image processor [10] 61 provides image processing capabilities to image capture system 25. For example, image processor [10] 61 may alter the resolution of the digitized image from ADC 33. Format processor 63 allows the data format of the digitized image to be changed prior to being stored in RAM 39 via bus 52. For example, format processor 63 may present the data representing the digitized image to RAM 39 in single or multi-bit per pixel format. Format processor 63 also communicates with a host via bus 52. --